

Fig. 1

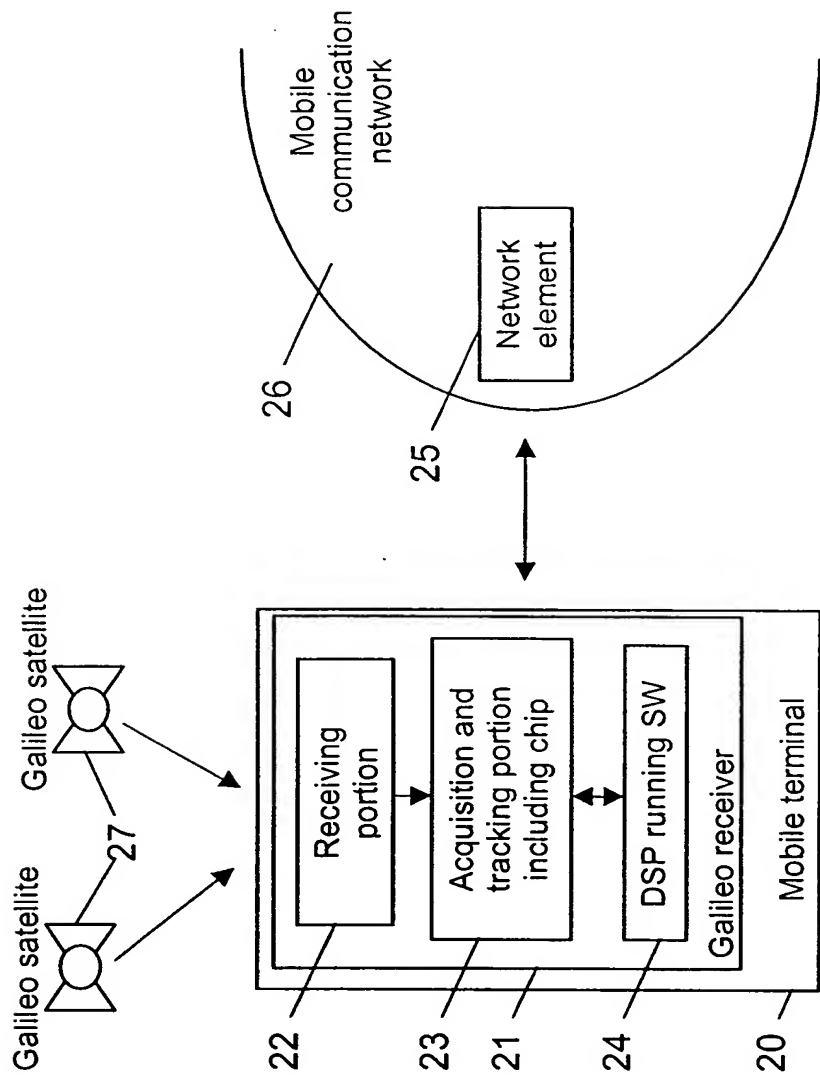


Fig. 2

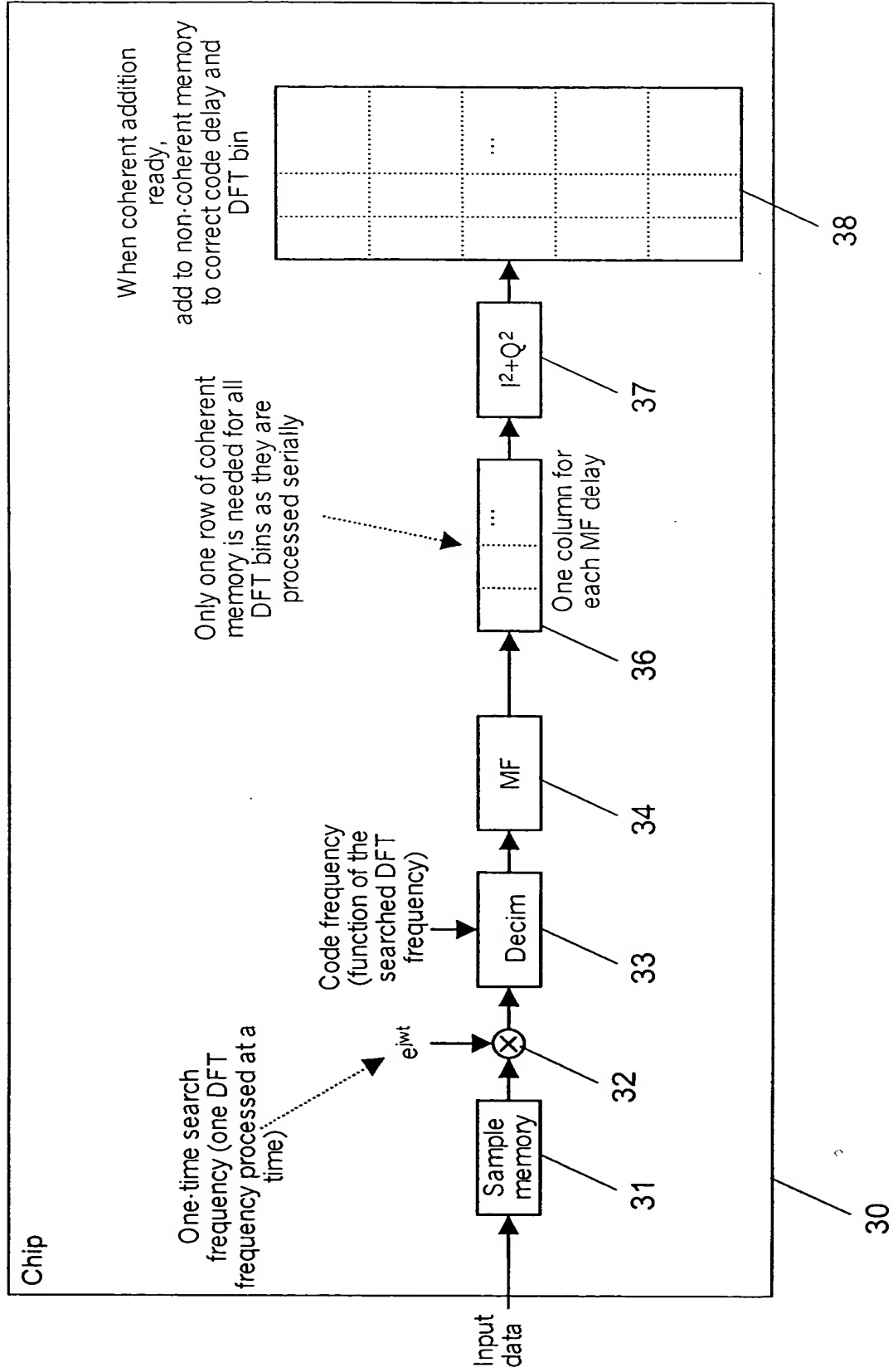


Fig. 3

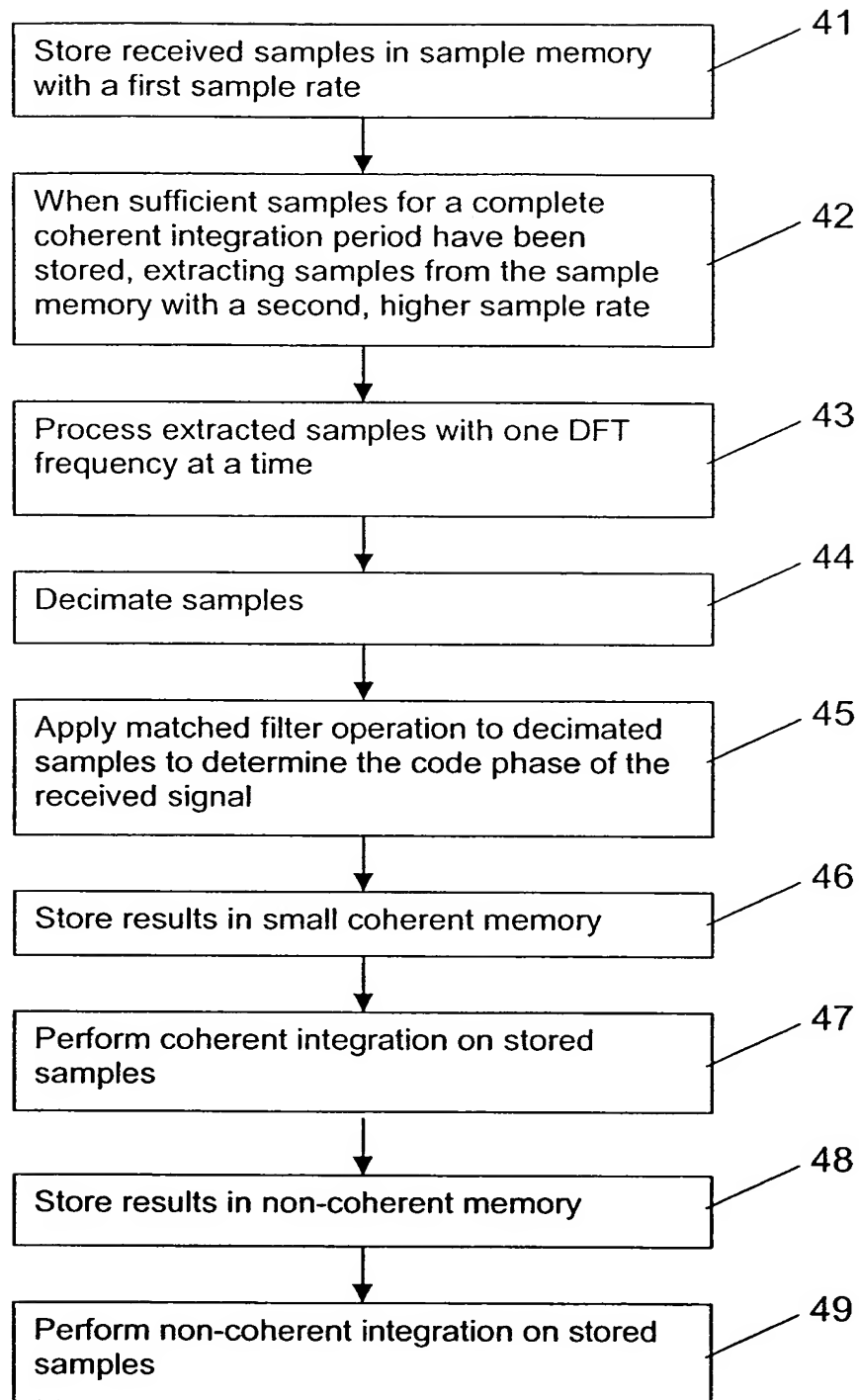


Fig. 4

	Prior art GPS C/A receiver	Proposed C/A receiver (*)
Sample Rate [MHz]	2	2
A/D wordlength (complex) [bits]	3	3
Correlators (complex)	8184	8184
DFT bins	17	1
Coherent Integration length [ms]	20	20
Coherent memory wordlength (complex) [bits]	8	8
Coherent + QuickShot memory [kBytes]	272	75

\* Calculated with 2X coherent integration length and 1 DFT bin, number of DFT bins in non-coherent integration is not limited

Fig. 5

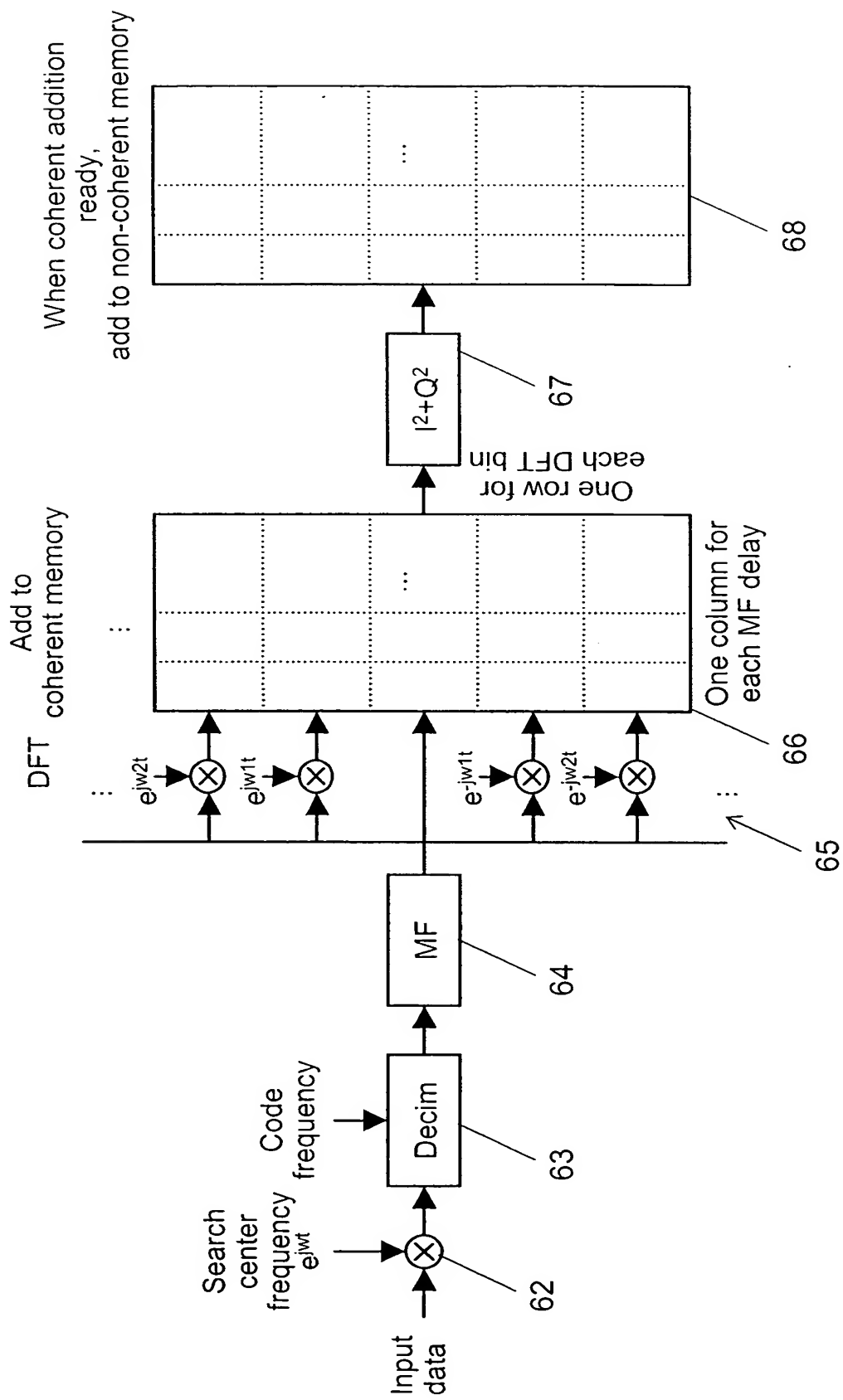


Fig. 6